



YEA SHIN TECHNOLOGY CO. , LTD

YS6906ZBB-A

N-CHANNEL ENHANCEMENT MOSFET

VDS=33V, ID=60A



DESCRIPTION

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

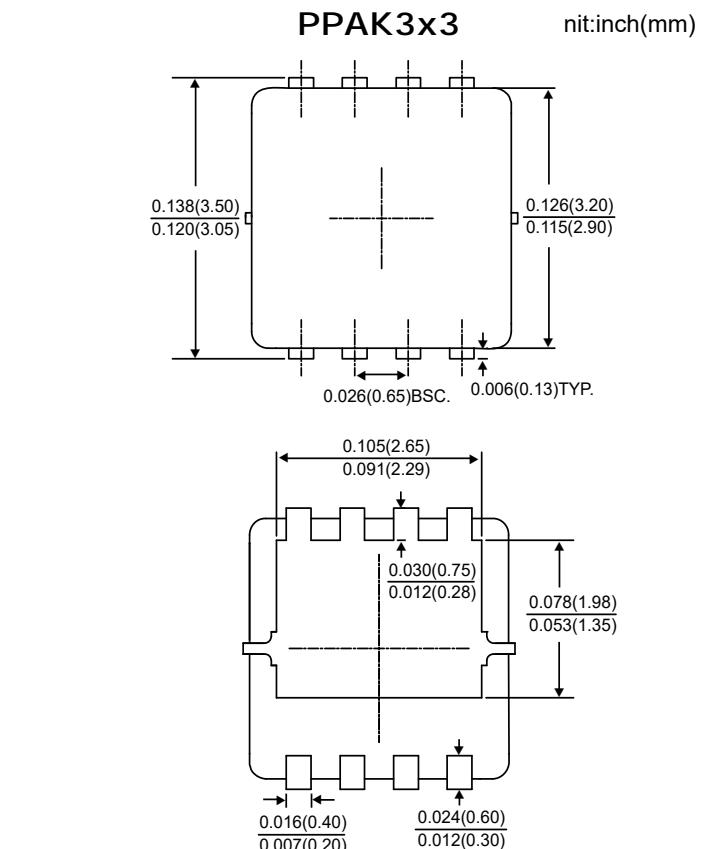
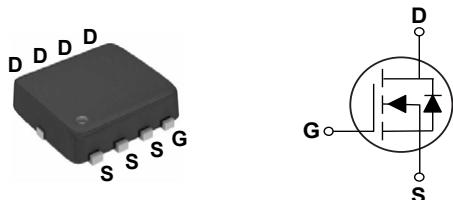
FEATURES

- $R_{DS(ON)} \leq 21\text{m}\Omega @ V_{GS}=10\text{V}$
- Improved dv/dt capability
- Fast switching
- Green Device Available
- AEC-Q101 qualified
- Marking : NG021、DC6906

APPLICATIONS

- Motor Drive
- Power Tools
- LED Lighting

PPAK3X3 PIN CONFIGURATION



Maximum Ratings @ $T_C=25^\circ\text{C}$ unless otherwise noted

Parameter	Symbol	Rating	Units
Drain-Source Voltage	V_{DSS}	60	V
Gate-Source Voltage	V_{GSS}	± 20	V
Drain Current - Continuous	I_D	33	A
		20	A
Drain Current - Pulsed (NOTE 1)	I_{DM}	132	A
Single Pulse Avalanche Energy (NOTE 2)	EAS	42	mJ
Single Pulse Avalanche Current (NOTE 2)	IAS	29	A
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	44.6	W
Thermal Resistance Junction to Ambient	$R_{\theta JA}$	62	$^\circ\text{C}/\text{W}$
Thermal Resistance Junction to Case	$R_{\theta JC}$	2.8	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	T_J, T_{STG}	-50 to +150	$^\circ\text{C}$

DEVICE CHARACTERISTICS

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Electrical Characteristics ($T_J=25^\circ\text{C}$ unless otherwise noted)

Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV_{DSS}	Drain-Source Breakdown Voltage	$V_{\text{GS}}=0\text{V}$, $I_D=250\mu\text{A}$	60	---	---	V
I_{DSS}	Drain-Source Leakage Current	$V_{\text{DS}}=60\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=25^\circ\text{C}$	---	---	1	μA
		$V_{\text{DS}}=48\text{V}$, $V_{\text{GS}}=0\text{V}$, $T_J=125^\circ\text{C}$	---	---	10	μA
I_{GSS}	Gate-Source Leakage Current	$V_{\text{GS}}=\pm 20\text{V}$, $V_{\text{DS}}=0\text{V}$	---	---	± 100	nA

On Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$R_{\text{DS(ON)}}$	Static Drain-Source On-Resistance	$V_{\text{GS}}=10\text{V}$, $I_D=15\text{A}$	---	17	21	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}$, $I_D=8\text{A}$	---	20	24	
$V_{\text{GS(th)}}$	Gate Threshold Voltage	$V_{\text{GS}}=V_{\text{DS}}$, $I_D=250\mu\text{A}$	1.2	1.8	2.2	V
g_{fs}	Forward Transconductance	$V_{\text{DS}}=10\text{V}$, $I_D=10\text{A}$	---	9	---	S

Dynamic and switching Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
Q_g	Total Gate Charge	$V_{\text{DS}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $I_D=15\text{A}$ (NOTE 3、4)	---	28	---	nC
Q_{gs}	Gate-Source Charge		---	3.5	---	
Q_{gd}	Gate-Drain Charge		---	6.5	---	
$T_{\text{d(on)}}$	Turn-On Delay Time	$V_{\text{DD}}=30\text{V}$, $V_{\text{GS}}=10\text{V}$, $R_G=6\Omega$, $I_D=1\text{A}$ (NOTE 3、4)	---	7.2	---	ns
T_r	Rise Time		---	38	---	
$T_{\text{d(off)}}$	Turn-Off Delay Time		---	34	---	
T_f	Fall Time		---	8.2	---	
C_{iss}	Input Capacitance	$V_{\text{DS}}=20\text{V}$, $V_{\text{GS}}=0\text{V}$, $F=1\text{MHz}$	---	1110	---	pF
C_{oss}	Output Capacitance		---	110	---	
C_{rss}	Reverse Transfer Capacitance		---	60	---	
R_g	Gate resistance	$V_{\text{GS}}=0\text{V}$, $V_{\text{DS}}=0\text{V}$, $F=1\text{MHz}$	---	2.2	---	Ω

Drain-Source Diode Characteristics and Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I_s	Continuous Source Current	$V_G=V_D=0\text{V}$, Force Current	---	---	33	A
I_{SM}	Pulsed Source Current		---	---	66	A
V_{SD}	Diode Forward Voltage	$V_{\text{GS}}=0\text{V}$, $I_s=1\text{A}$, $T_J=25^\circ\text{C}$	---	---	1	V

NOTES :

- Repetitive Rating : Pulsed width limited by maximum junction temperature.
- $V_{\text{DD}}=25\text{V}$, $V_{\text{GS}}=10\text{V}$, $L=0.1\text{mH}$, $I_{\text{AS}}=29\text{A}$, $R_G=25\Omega$, Starting $T_J=25^\circ\text{C}$.
- The data tested by pulsed, pulse width $\leq 300\text{us}$, duty cycle $\leq 2\%$.
- Essentially independent of operating temperature.

DEVICE CHARACTERISTICS

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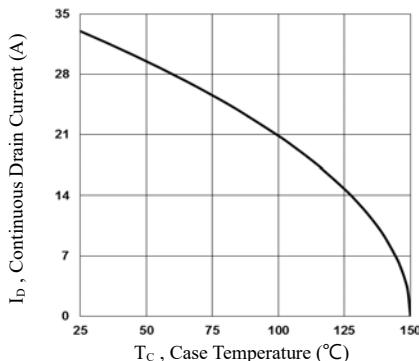


Fig.1 Continuous Drain Current vs. T_c

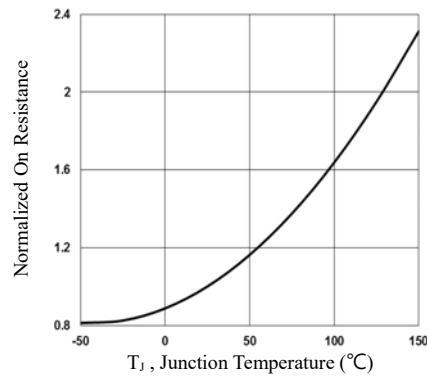


Fig.2 Normalized RDSON vs. T_J

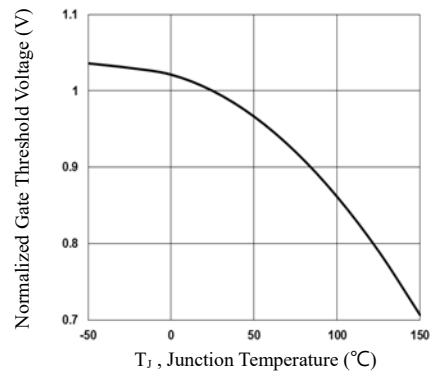


Fig.3 Normalized V_{th} vs. T_J

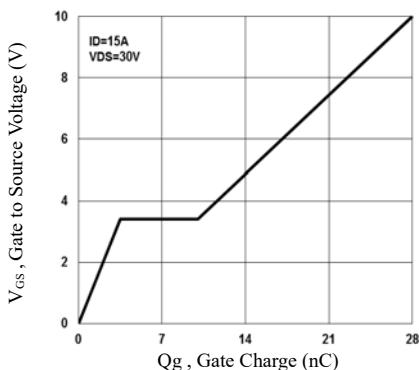


Fig.4 Gate Charge Waveform

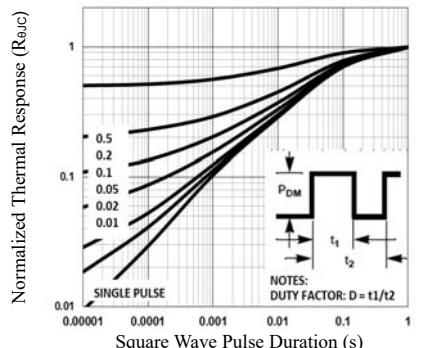


Fig.5 Normalized Transient Impedance

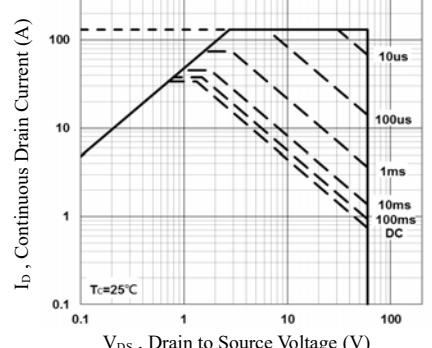


Fig.6 Maximum Safe Operation Area

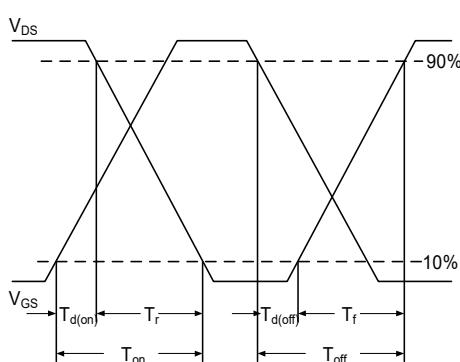


Fig.7 Switching Time Waveform